



2811

COPY OF PAPERS
ORIGINALLY FILEDPATENT
Attorney Docket No. ASC-044**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Fitzgerald and Gerrish

SERIAL NUMBER: 09/884,172

ART UNIT: 2811

FILING DATE: June 19, 2001

EXAMINER: Not yet assigned

TITLE: Method of Fabricating CMOS Inverter And Integrated Circuits
Utilizing Strained Silicon Surface Channel-MOSFETs**CERTIFICATE OF FIRST CLASS MAILING UNDER 37 C.F.R. 1.8**

I hereby certify that this correspondence, and any documents referred to as enclosed herein, are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on this 24th day of July, 2002.

Date

July 24, 2002

Brenda T. Kowalczyk

Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith is/are:

Transmittal Form (1 pg.); Executed Power of Attorney by Assignee of
Entire Interest (Revocation of Prior Powers and New Power of Attorney)
(2 pgs.); and a postcard.

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TRANSMITTAL
FORM

Application Serial Number	09/884,172
Filing Date	June 19, 2001
First Named Inventor	Fitzgerald
Group Art Unit	2811
Examiner Name	Not yet assigned
Attorney Docket No.	ASC-044
Patent No.	Not applicable
Issue Date	Not applicable

ENCLOSURES (check all that apply)

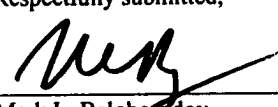
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